

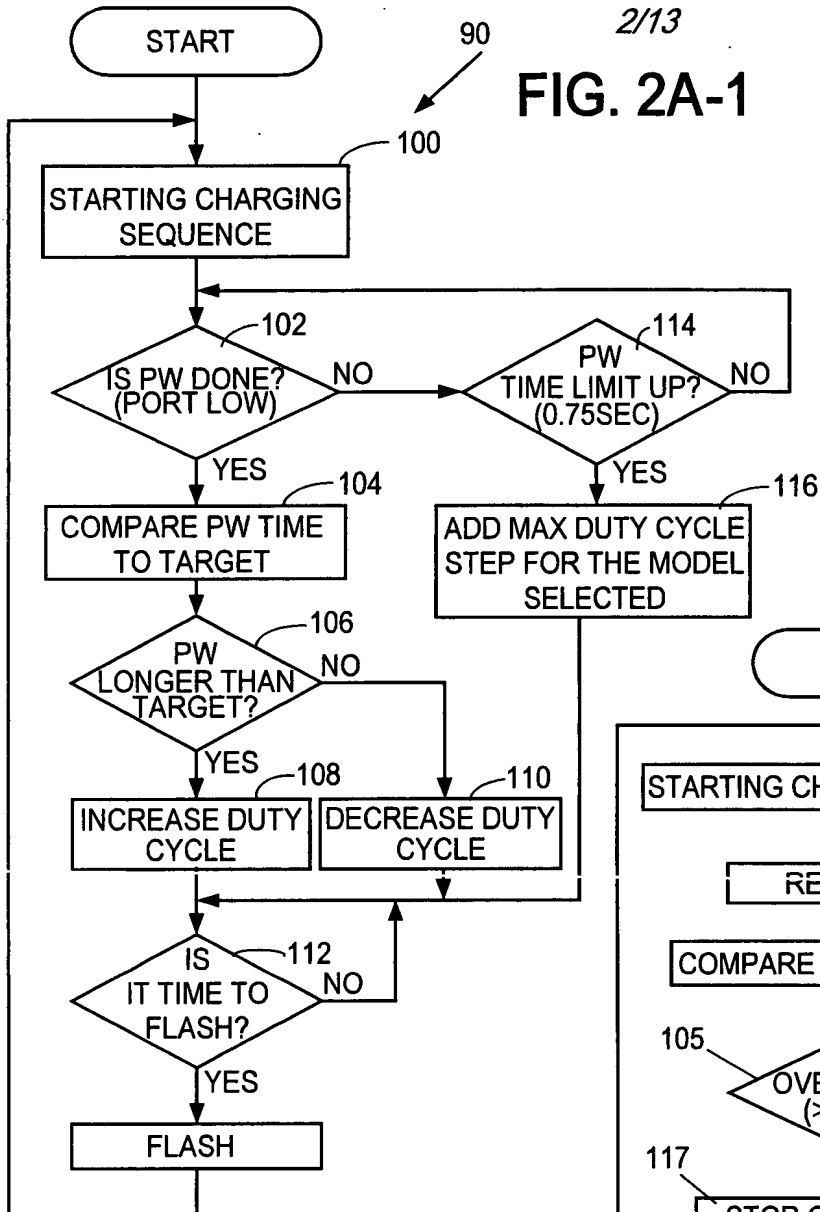
**FIG. 1**

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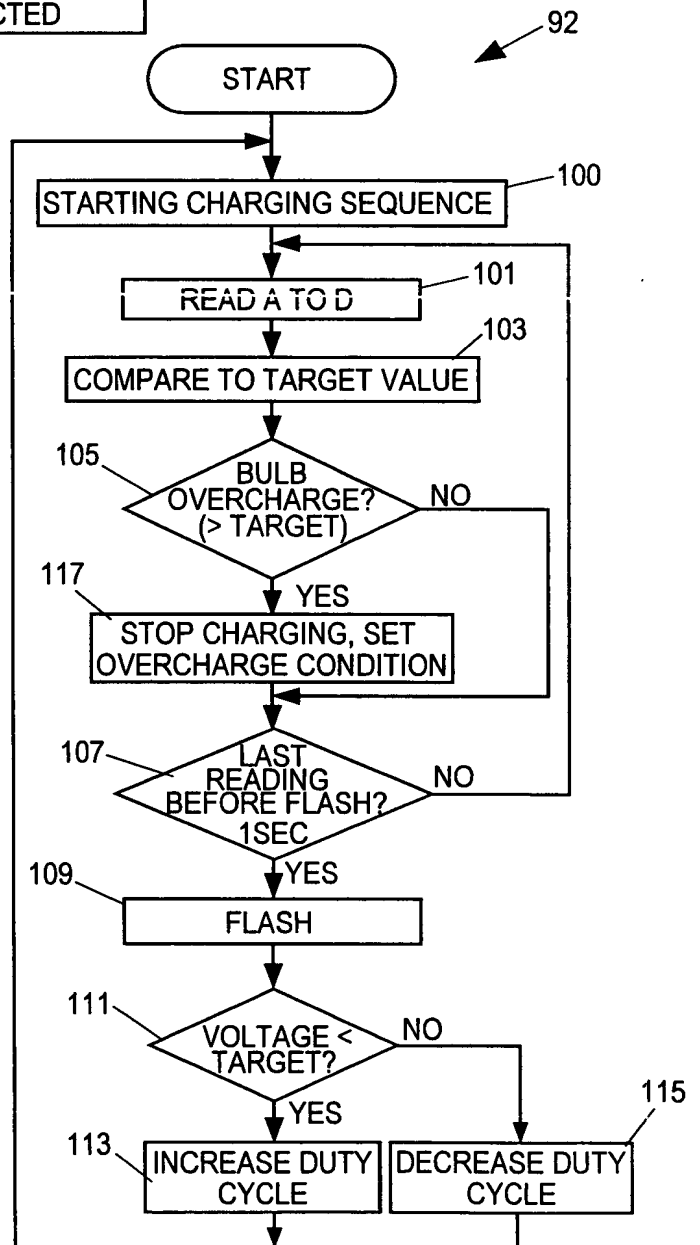
The diagram illustrates a microprocessor-based electronic device, labeled FIG. 1. The central component is a **MICROPROCESSOR CENTRAL PROCESSING UNIT 12**. It is connected to several peripheral blocks:

- Power and Reset:** A **POWER REGULATOR 14** receives **POWER INPUT P** and provides **VDD** to the microprocessor. **IC RESET COMPONENTS** and a **VDD & INTERNAL RESET** block are also connected to the microprocessor's reset pins.
- Memory:** **ROM 12a** and **RAM 12b** are connected to the microprocessor's memory ports.
- Input/Output and Control:**
  - POWER SWITCHING CONTROL CIRCUITRY 16** and **CHARGING CONTROL CIRCUITRY 18** are connected to the microprocessor's control lines.
  - POWER SWITCHING PORT 16a** and **CHARGE DRIVER PORT 18a** are connected to the microprocessor's I/O ports.
  - SYNC PULSE & FWR SENSING PORT 14a** and **14b** are connected to the microprocessor's sensing ports.
  - PULSE WIDTH DETECT PORT 24a** is connected to the microprocessor's I/O port.
  - MODEL SELECT SWITCH 30** is connected to the microprocessor's model select pin.
  - TONE & TEMPORAL CONTROL SWITCH 32** is connected to the microprocessor's tone and temporal control pin.
- Timing and Oscillation:** **CLOCK INPUTS** and an **OSCILLATOR** are connected to the microprocessor's clock input.
- Output Drivers:**
  - TONE GENERATING PORT 26a** is connected to the **HORN DRIVER CIRCUITRY 26**.
  - BULB DRIVER PORT 22a** is connected to the **BULB FIRING CIRCUITRY 22**.
- Capacitor and Flash Elements:** **CAP. VOLTAGE DIVIDER CIRCUITRY A TO D OPTION 24-2** and **VOLTAGE TO PULSE WIDTH OPTION 24-1** are connected to the microprocessor's I/O ports. These are linked to **20-2** and **20-1** respectively, which are part of the **STORAGE CAPACITOR & FLASH BULB ELEMENTS 20**.

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**FIG. 2A-1**

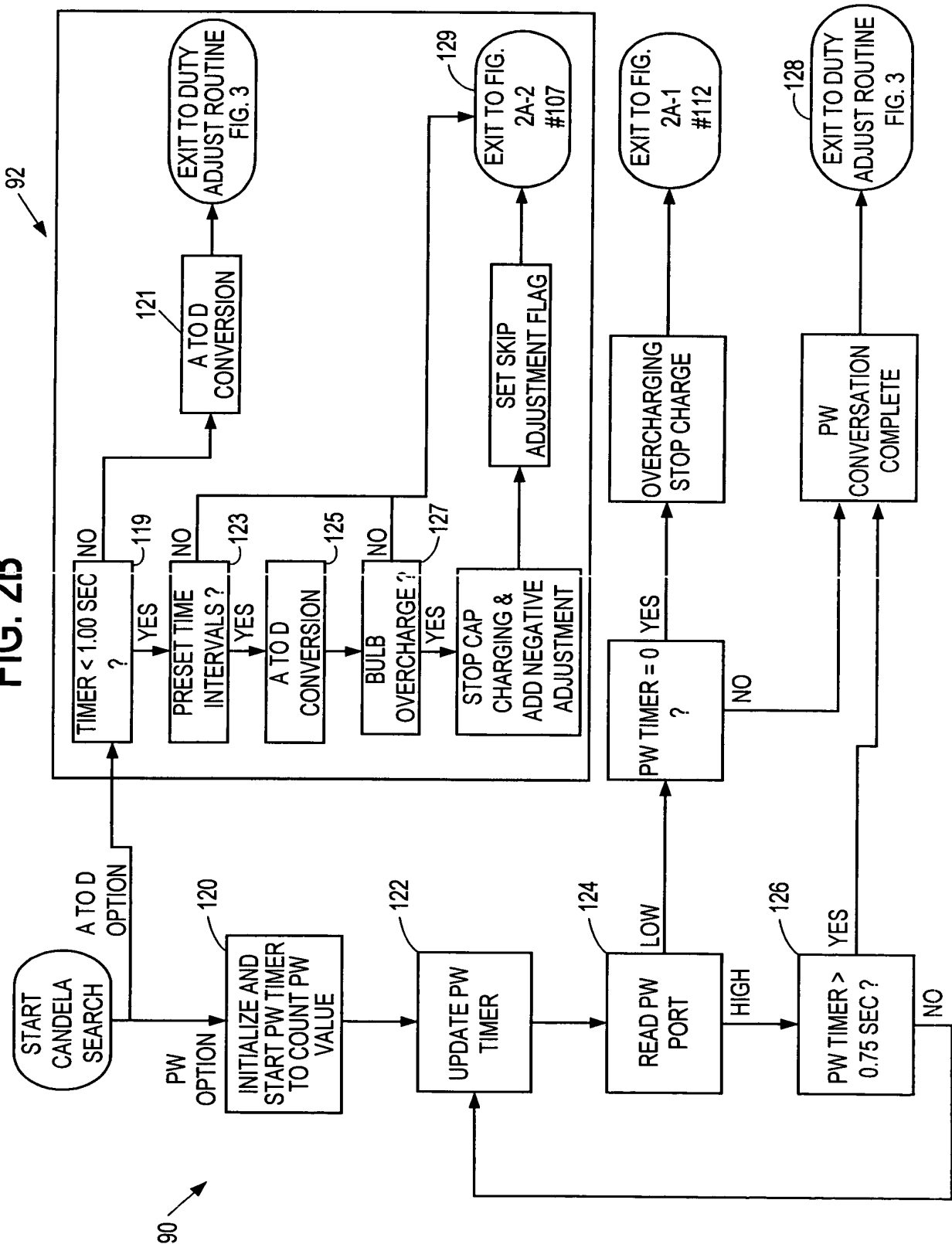


**FIG. 2A-2**



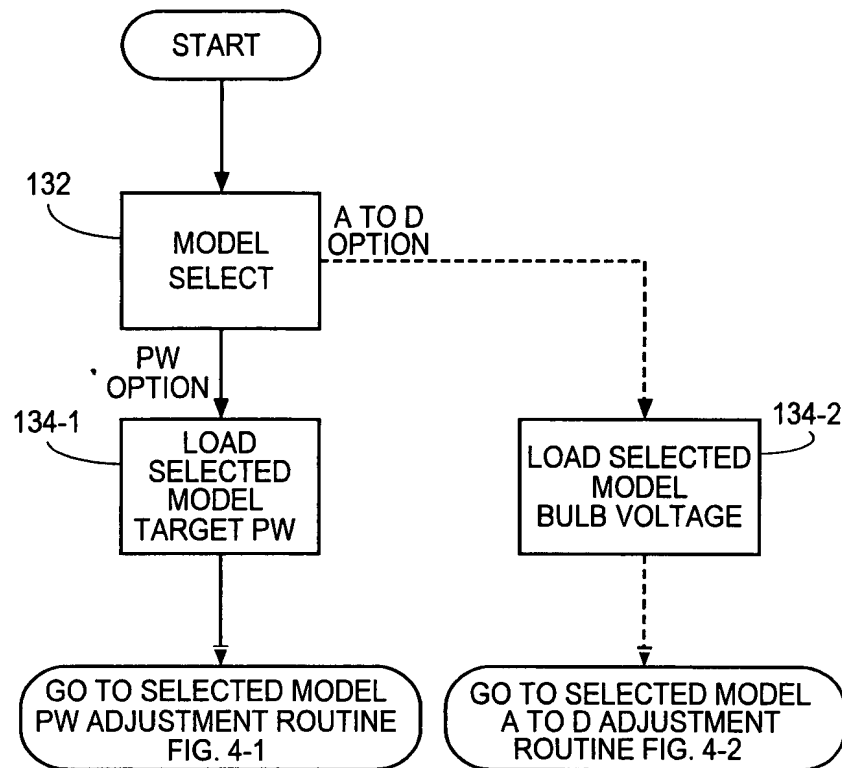
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FIG. 2B



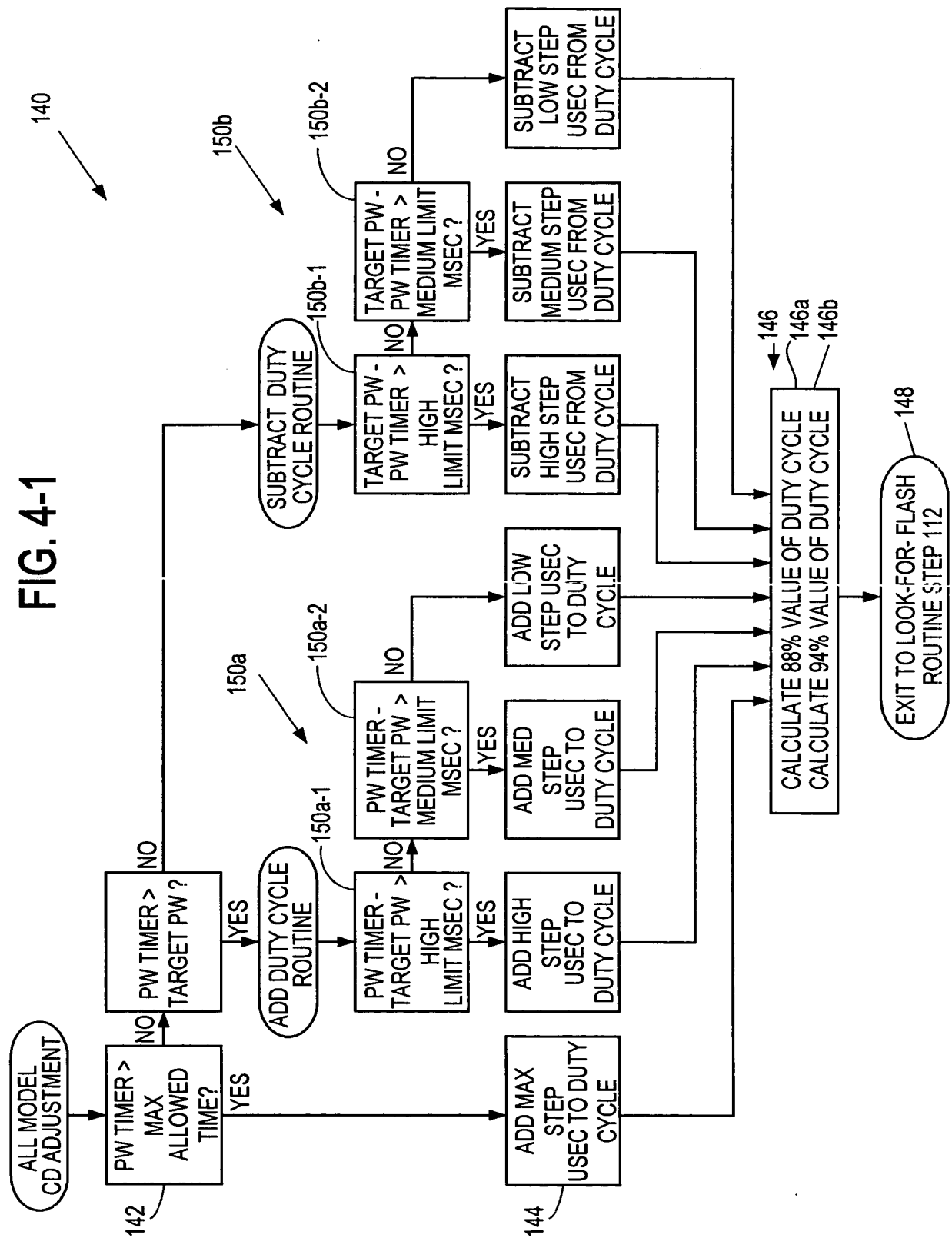
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FIG. 3



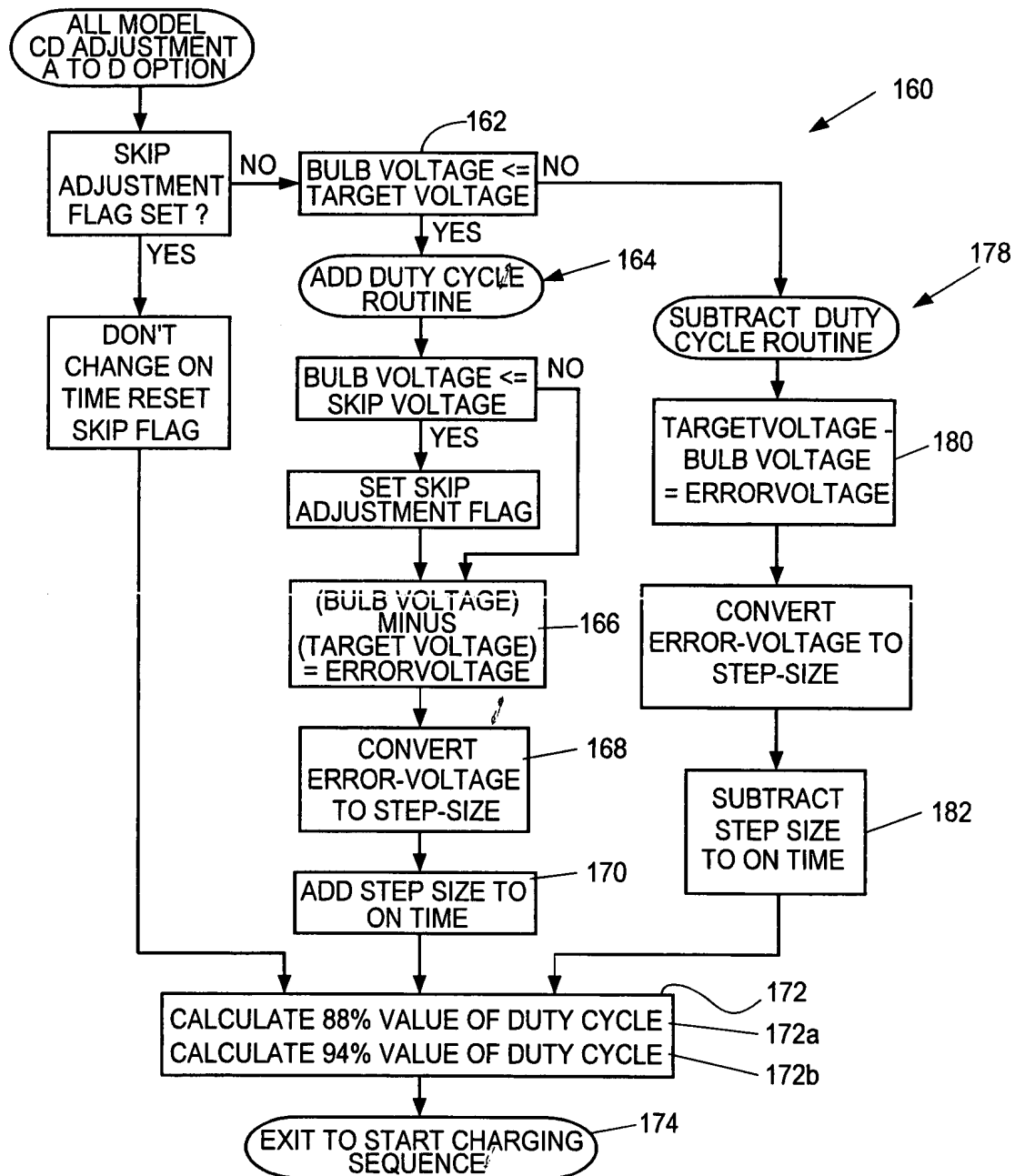
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FIG. 4-1



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FIG. 4-2



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FIG. 5-1

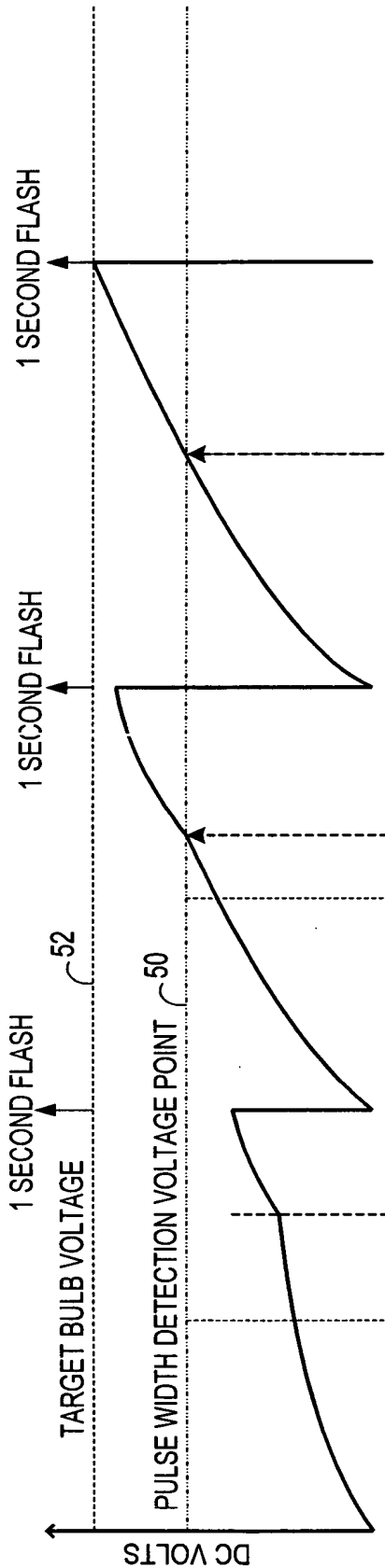


FIG. 5-2

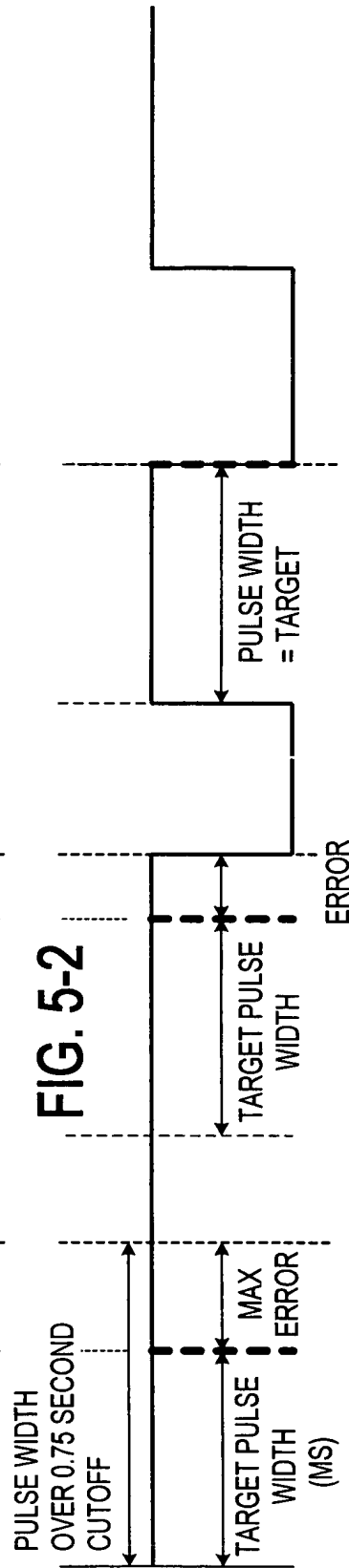
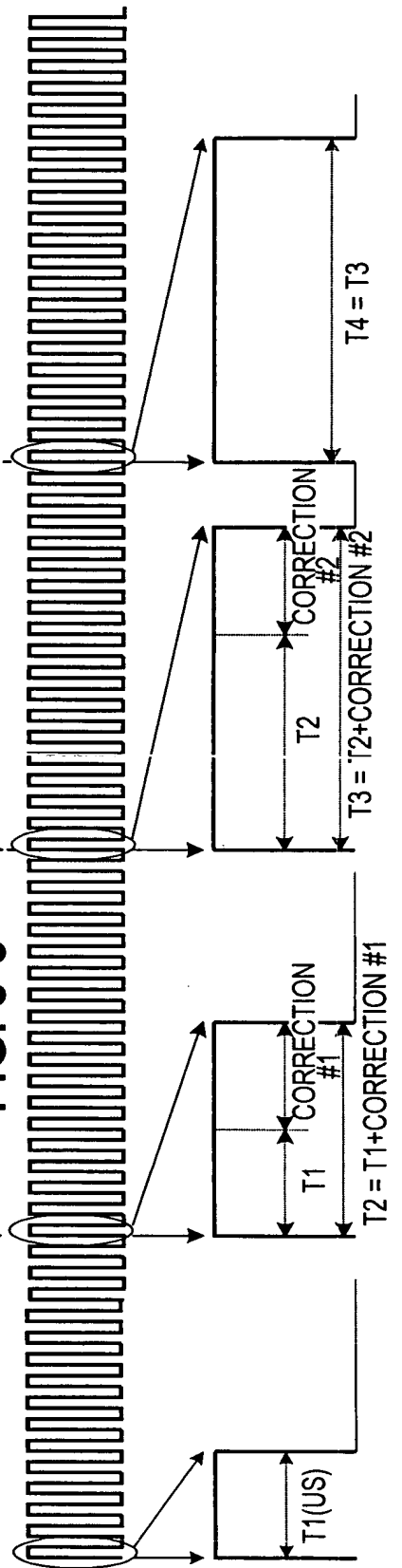


FIG. 5-3



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FIG. 6-1

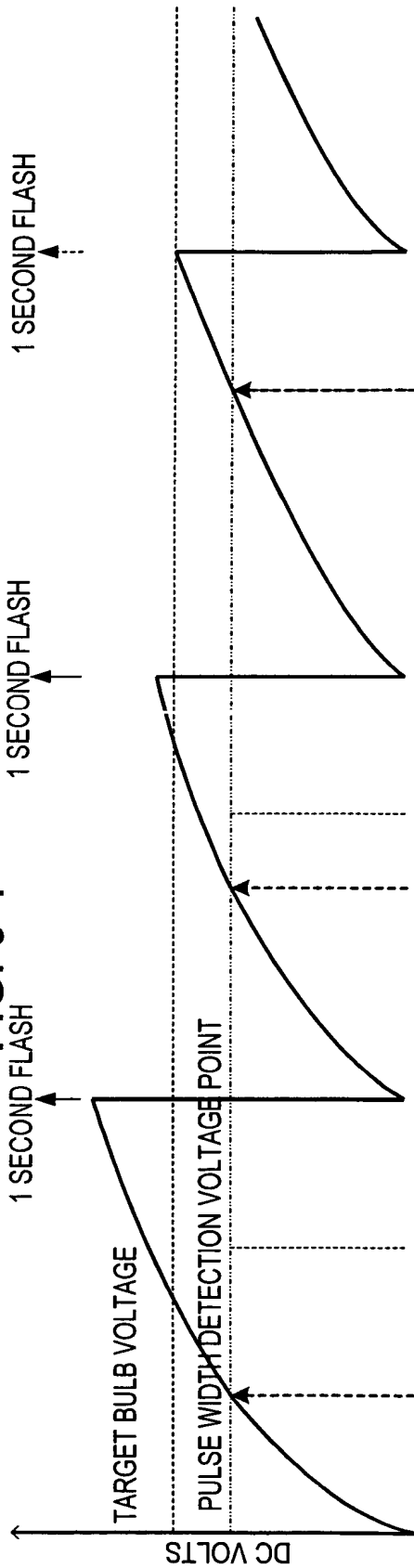


FIG. 6-2

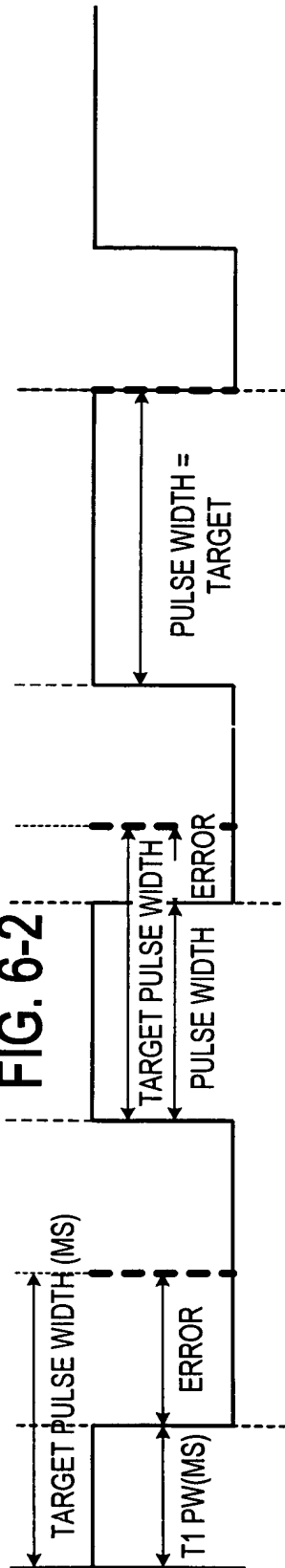
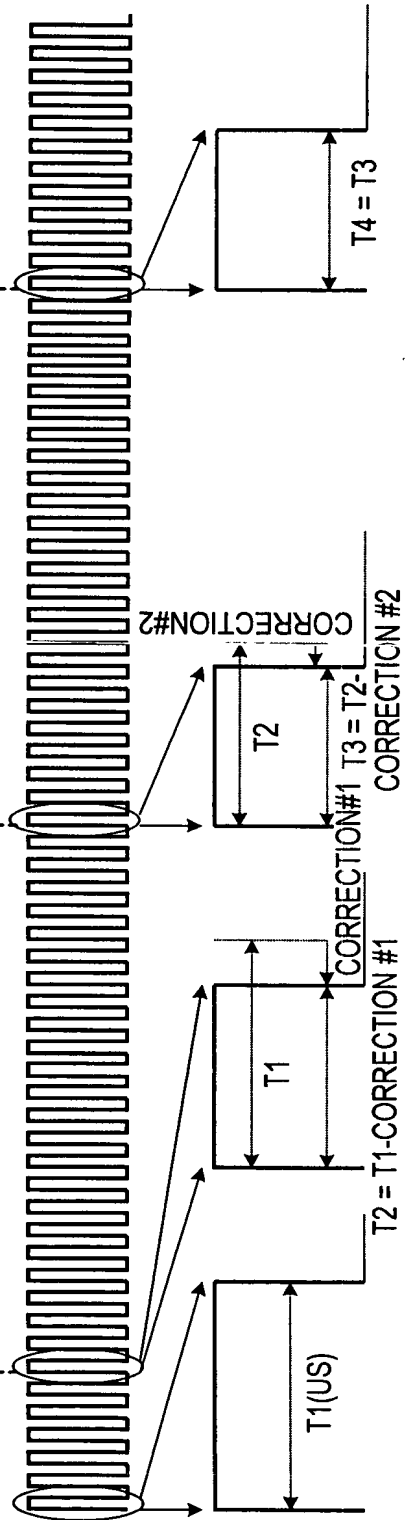


FIG. 6-3





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FIG. 7-1

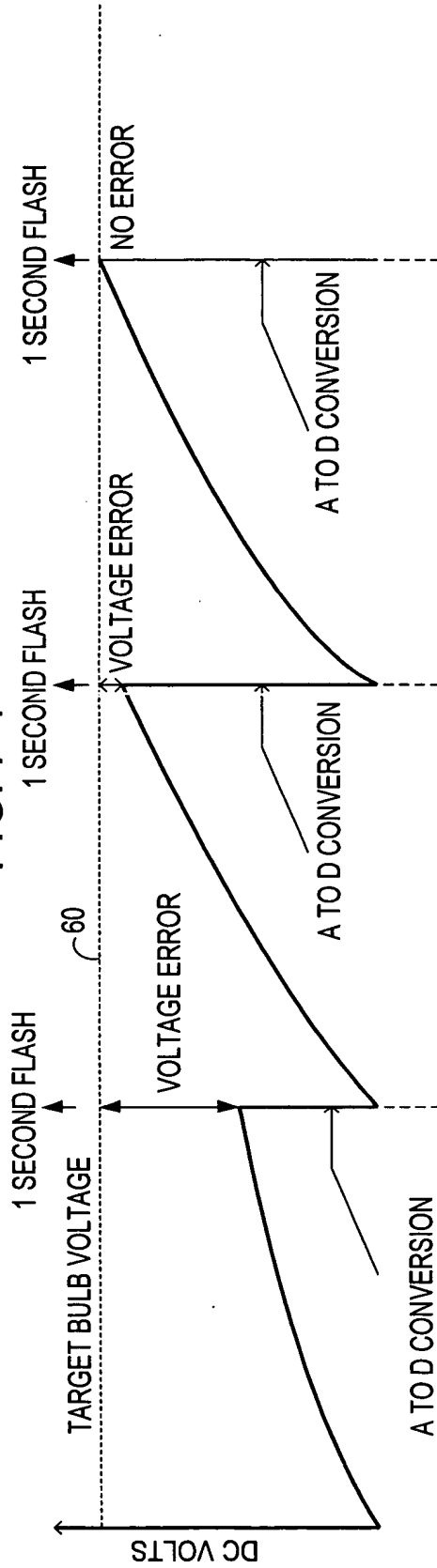
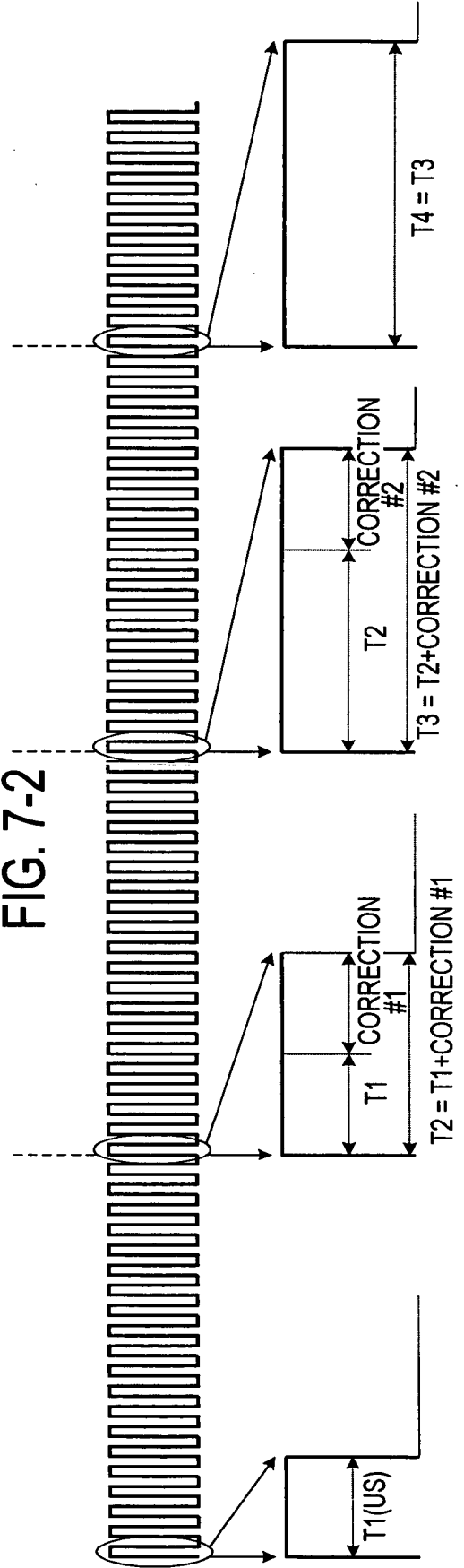


FIG. 7-2



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FIG. 8-1

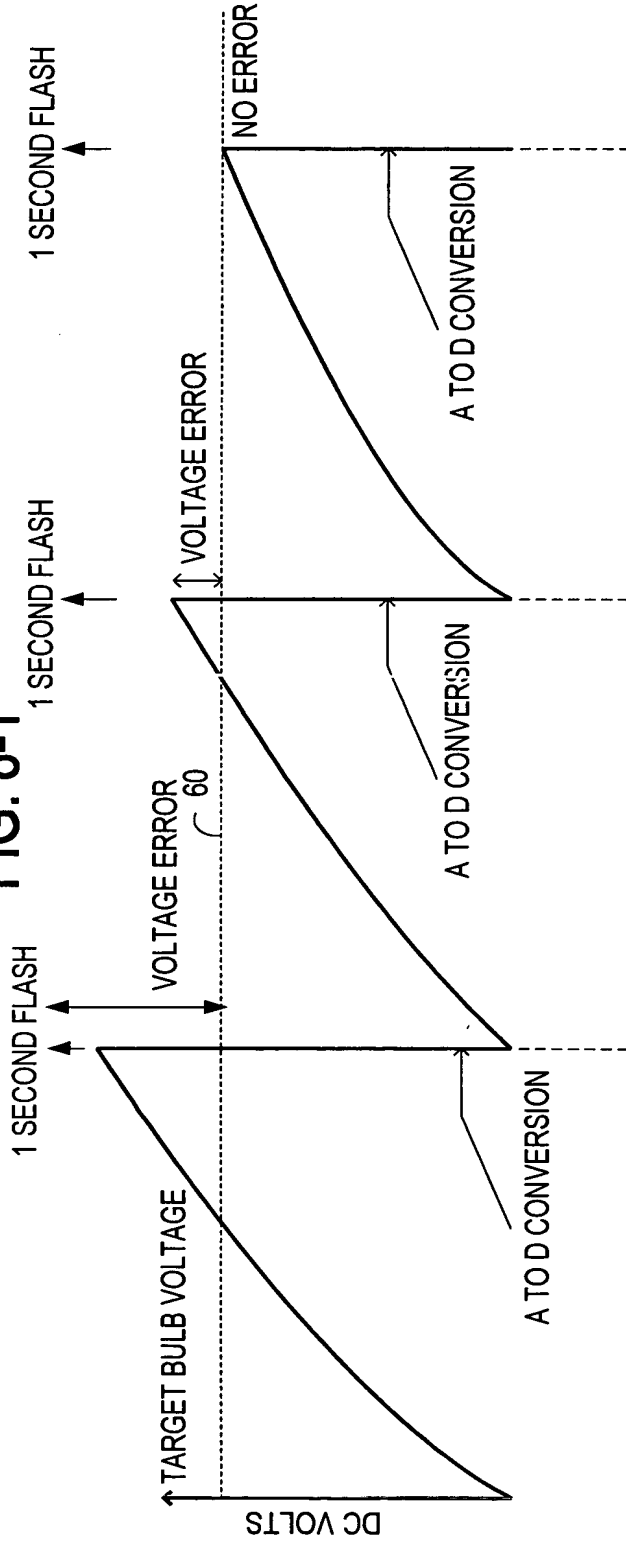
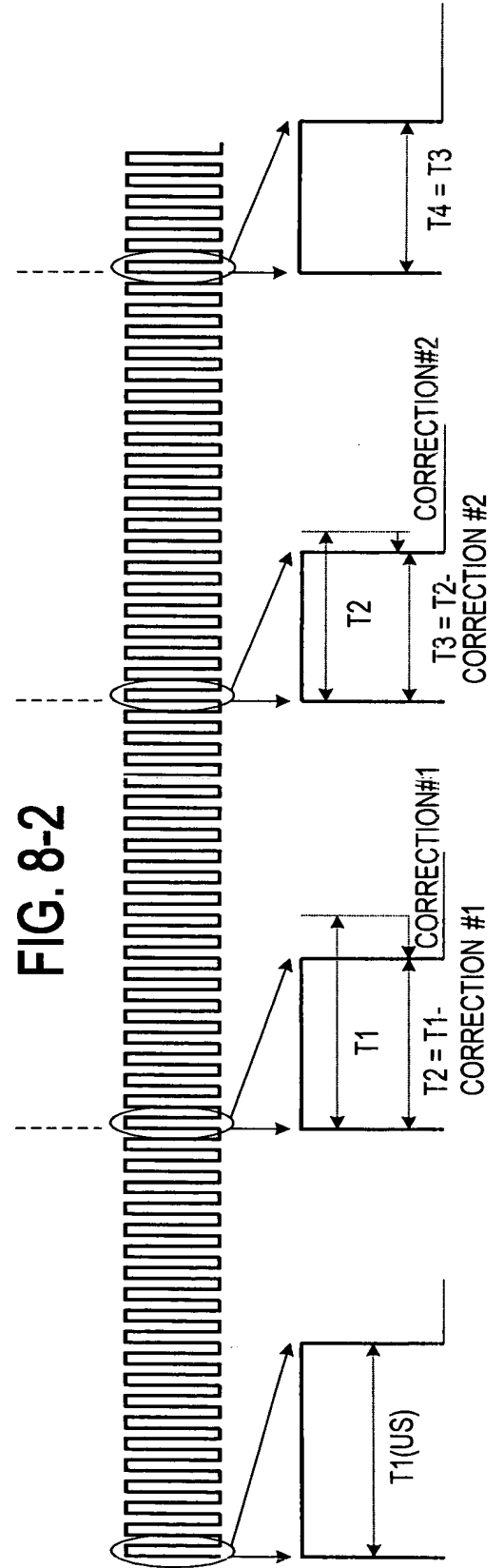


FIG. 8-2



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FIG. 9

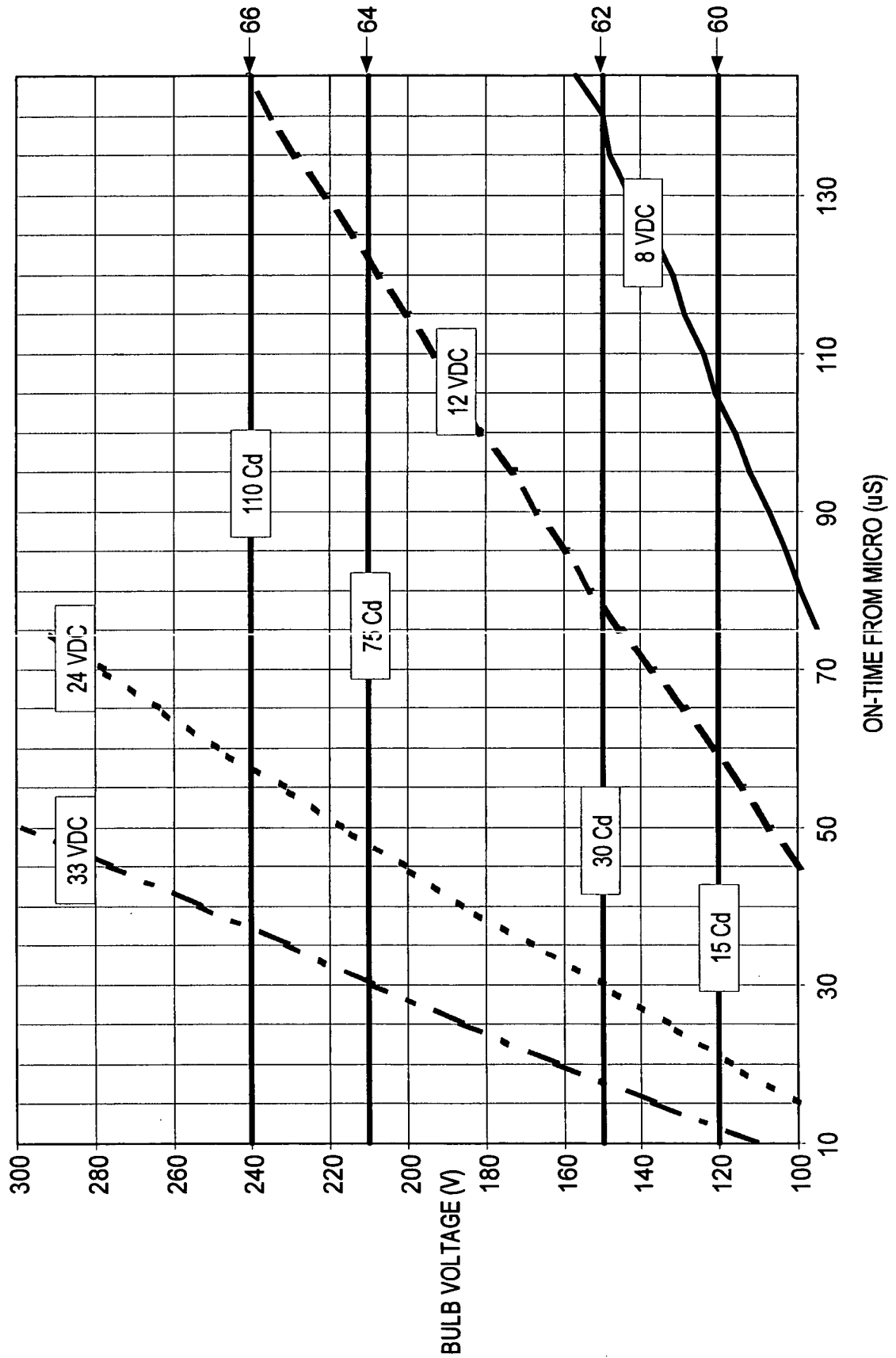
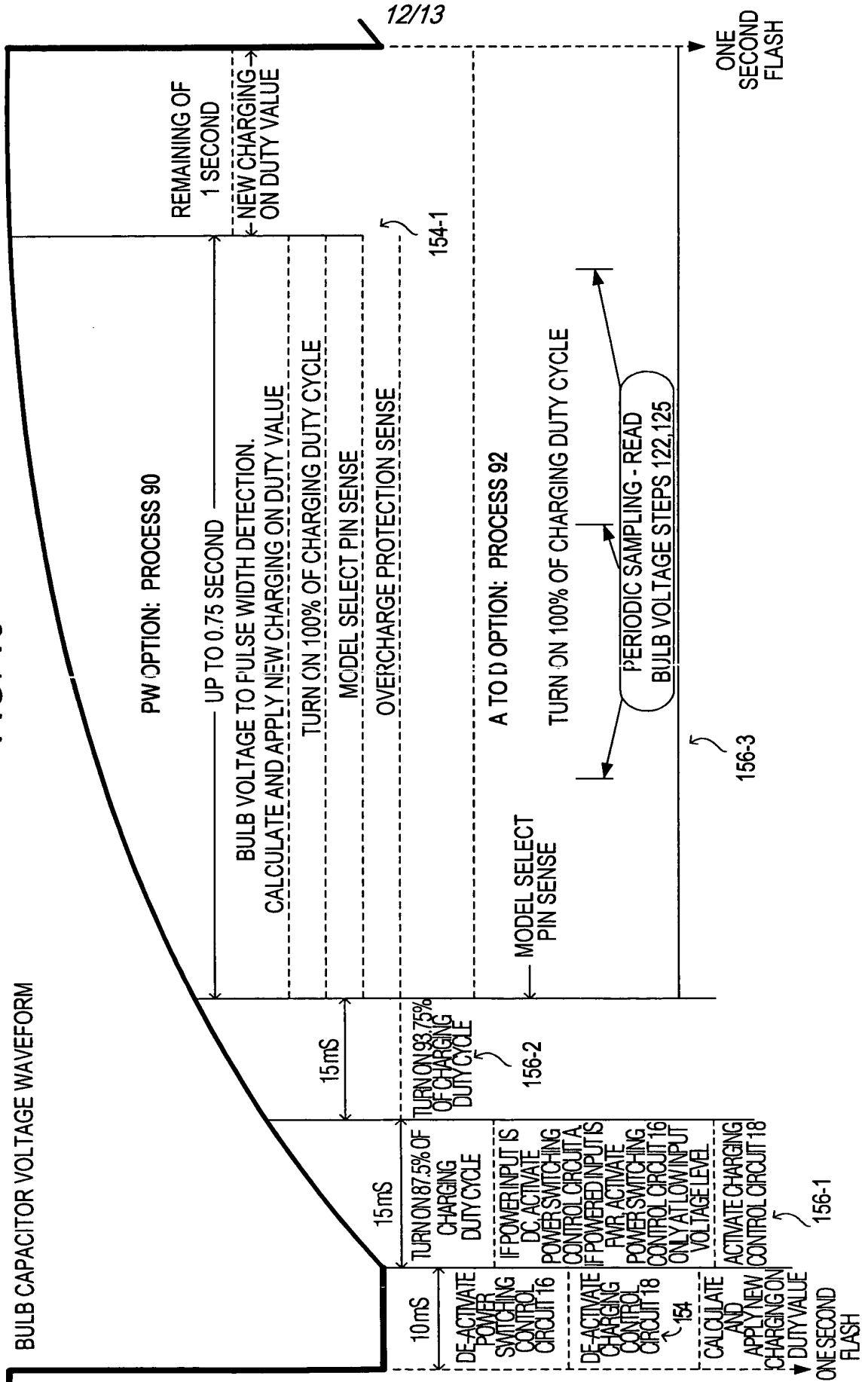


FIG. 10



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FIG. 11

